

ASX RELEASE

29 November 2023

Company update and 2024 imec contract extension

4DS Memory Limited (ASX:4DS) (**4DS**) (the **Company**), is pleased to provide a summary of the 2023 Company technology achievements, the imec 2024 contract extension and the appointment of a strategic advisor.

Company Technology Achievements for 2023

On 23 August 2023, the Company announced that, as a consequence of all the development activities which had taken place since 5 October 2022, it had now demonstrated the successful transfer of all new process improvements and learning cycles developed at the Stanford Nanofabrication Facility into imec's megabit array.

These process improvements included modification of the PCMO etch process and the composition of the memory cells, and it validated that the technology optimization was transferable from fab to fab.

After extensive analysis 4DS showed, for the first time, a fully functioning megabit array with 60nm memory cells, access transistors and write circuitry.

On 18 September 2023, the Company informed shareholders that it had successfully completed further analysis of the Fourth Platform Lot and the results obtained were significantly better as compared to results announced on 23 August 2023.

The Company confirmed that following additional testing within the megabit array 4DS had shown:

- Write speeds at 9.5 nanoseconds which were significantly superior to DRAM;
- Endurance in excess of 3 billion cycles;
- DRAM read speed; and
- Retention is persistent and tuneable.

4DS' ReRAM performance profile to suitably meet these outcomes had again been clearly demonstrated on the Fourth Platform Lot via the additional testing.

imec 2024 Collaboration Agreement

Following lengthy strategic discussions, 4DS and imec have today finalised the terms of the collaboration extension which will now commence on 1 January 2024 and run to end of the same year.

4DS and imec have agreed to additional collaboration activities to be undertaken during 2024. The consideration for all collaboration activities is 1.92 million Euro.

imec manufacture of Fifth and Sixth Platform Lots

Process optimization and short loop analysis will take place over the balance of 2023 and into early 2024.

The Fifth and Sixth Platform Lots will again be manufactured on imec's unique Dory platform on 60nm and significantly for the first time 20nm memory cells.

Manufacturing of the Fifth and Sixth Platform Lots will commence during Q1 2024 and Q2 2024 respectively with delivery to 4DS expected during Q3 2024.

The Fifth Platform Lot is seeking to further optimise the unique characteristics of the results already seen on the 60nm memory cells prior to undertaking manufacturing of the Sixth Platform Lot on 20nm memory cells.

4DS' Executive Chairman, Mr David McAuliffe stated "Today's ever-growing computing landscape presents a growing challenge to solve for platforms, applications and system designers, with massive shifts taking place, including the rise of Generative AI, and other related advances, resulting in severe processing bottlenecks.

"With a strong balance sheet and a much-reduced dependency on raising additional capital, unless it was a strategic placement, the Board is of the view that this increased investment at imec is in the best interests of shareholders to potentially maximise the opportunity to further position 4DS as a unique technology to address gaps in the market."

Further, Mr David McAuliffe stated, "In light of this significant investment and the potential outcomes in 2024, we are delighted to announce the appointment of Mr Peter Himes as a strategic advisor to the Board."

Mr Peter Himes Appointment as Strategic Advisor to the 4DS Board

Mr Peter Himes is an experienced senior executive in high growth technology firms, with extensive general management responsibilities. He is agile at building winning teams, defining and addressing new markets, building key relationships and partnerships all across the value chain, and promoting and evangelizing for business opportunities. Peter is consistently recognized as a thought leader across multiple industries with a strong focus on innovation systems and strategic alliances over the past 10 years. The appointment is effective 1 December 2023.

Mr David McAuliffe stated "Peter is a highly regarded and well-respected individual in Silicon Valley with an exceptional track record. He will be a valued member of the 4DS team and a critical decision maker in how the Company determines commercialisation strategies on the back of potential positive results in 2024 on both 60nm and 20nm memory cells."

4DS Interactive Investor Hub

If you have any questions on this announcement or any past 4DS announcements, check out our Investor Hub. Like, comment, ask a question and view video summaries on important announcements. Shareholders can link through to the Investor Hub via: announcements.4dsmemory.com

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Authorised for release by the Board.

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About 4DS

4DS Memory Limited (ASX: 4DS), with facilities located in Silicon Valley, is a semiconductor development company of non-volatile memory technology, pioneering Interface Switching ReRAM for next generation gigabyte storage in mobile and cloud. Established in 2007, 4DS owns a patented IP portfolio, comprising 34 USA patents granted which have been developed in-house to create high-density Storage Class Memory. 4DS has a joint development agreement with Western Digital subsidiary HGST, a global storage leader, which accelerates the evolution of 4DS' technology. 4DS also collaborates with imec, a world-leading research and innovation hub in nanoelectronics and digital technologies. The combination of imec's widely acclaimed leadership in microchip technology and profound software and information and communication technology expertise makes them unique.

For more information, please visit www.4dsmemory.com.

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