

ASX RELEASE

6 June 2025

NOTICE UNDER SECTION 708A OF THE CORPORATIONS ACT

This notice is given by 4DS Memory Limited under section 708A(5)(e) of the *Corporations Act 2001* (Cth) ("Act").

The Company has today issued 500,000 fully paid ordinary shares in the capital of the Company ("Issued Shares"), at an issue price of \$0.055 per Issued Share following the exercise of 500,000 unlisted 31 March 2026 options with an exercise price of \$0.055 each.

Pursuant to section 708A(5)(e) of the Act, the Issuer gives notice that:

- a) the Issued Shares were issued without disclosure to investors under Part 6D.2 of the Act;
- b) the Issuer is providing this notice under paragraph (5)(e) of section 708A of the Act;
- c) as at the date of this notice, the Issuer has complied with:
 - i. the provisions of Chapter 2M of the Act, as they apply to the Issuer; and
 - ii. sections 674 and 674A of the Act; and
- d) as at the date of this notice, other than as set out below, there is no information that is 'excluded information' within the meaning of sections 708A(7) and 708A(8) of the Act which is required to be disclosed by the Issuer.

The Company refers to its announcement dated 27 May 2025, being a response to an ASX Price Query, its March 2025 Quarterly Activity Report and Appendix 4C announced in March 2025, pursuant to which the Company advised that its Executive Chairman, Mr David McAuliffe, planned to travel to the Company's facilities in the USA in early June 2025 whilst the Sixth Platform Lot was being analysed and to liaise with both Infineon Technologies LLC personnel present in the USA and with European imec senior representatives. The Company advises that Mr McAuliffe intends to travel to the USA on Monday, 9 June 2025 and that the Sixth Platform analysis is ongoing and that preliminary analysis is expected to be completed by no later than the end of June 2025. The Company will make further announcements if these timelines are not achieved and will continue to update shareholders in accordance with its continuous disclosure obligations.

Authorised for release by the 4DS Board.

ENDS**Contact information**

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About 4DS

4DS Memory Limited (ASX: 4DS), with facilities located in Silicon Valley, is a semiconductor technology company bringing high bandwidth, high endurance, persistent non-volatile memory to advanced CMOS process nodes. Its technology, known as Interface Switching ReRam, features tunable persistence and low energy per bit for today's most challenging computer intensive and AI processor applications. Established in 2007, 4DS owns a patented IP portfolio, comprising 34 USA patents, and is the first company to develop Area based ReRam, also known as PCMO, on an advanced CMOS processing node. 4DS has a joint development agreement with Western Digital subsidiary HGST, a global leader in storage solutions, and a development agreement with Belgium-based imec - a world leading research and innovation hub in nano electronics and digital technologies.

For more information, please visit www.4dsmemory.com.